

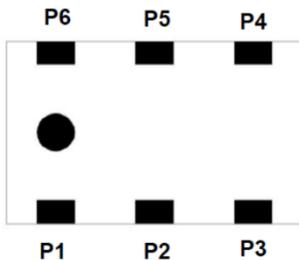
Features

- Multilayer monolithic construction yields high reliability
- Low insertion loss and small size SMD chip design
- Can simplify your complex tuning and circuit design
- LTCC process

Specifications

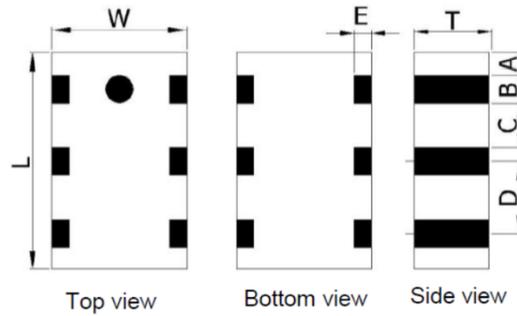
NO.	Items	Spec	Unit
1	Frequency range	2800~6000	MHz
2	Unbalanced Impedance	50	Ohms
3	Differential Balanced Imp.	50	Ohms
4	Return Loss at Unbalanced Port	9.5 min.	dB
5	Insertion Loss	1.5 max.	dB
6	Amplitude Difference	1.8 max.	dB
7	Phase Difference	180 ± 12	deg
8	CMRR	15 min.	dB
9	Power Capacity (CW)	3 max.	W
Operating & Storage Condition (Component)			
Operation Temperature Range: -40°C ~ +85°C			
Storage Temperature Range: -40°C ~ +85°C			
Storage Condition before Soldering (Included packaging material)			
Storage Temperature Range: +5 ~ +40 °C			
Humidity: 30 to 70% relative humidity			

Construction



P1	Unbalance Port	P4	Balance Port
P2	GND or DC feed + RF GND	P5	GND
P3	Balance Port	P6	NC

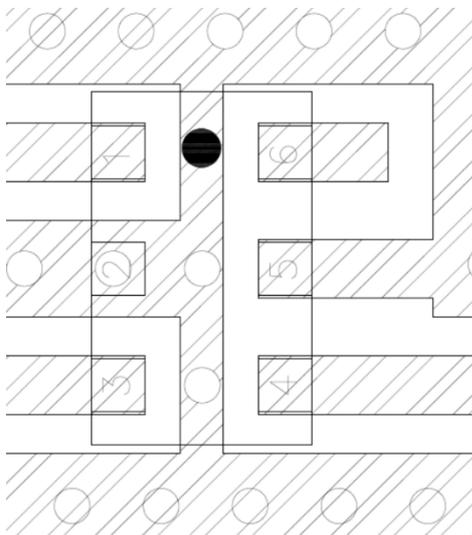
Dimensions



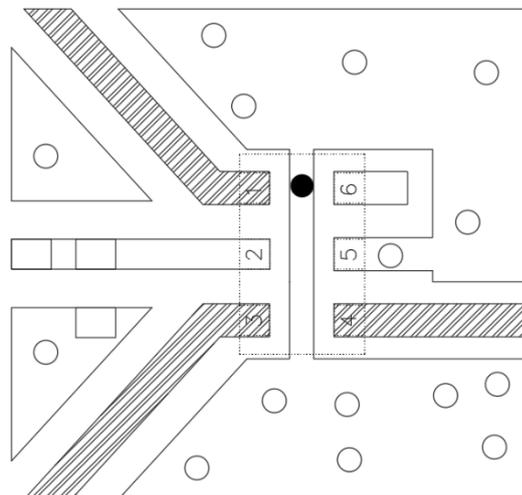
Symbol	L	W	T	A	B	C	D	E
Dimension (mm)	2.0 ± 0.15	1.25 ± 0.15	0.95 ± 0.15	0.20 ± 0.1	0.30 ± 0.1	0.35 ± 0.1	0.65 ± 0.1	0.25 ± 0.1

Mounting Considerations

Without DC Feed

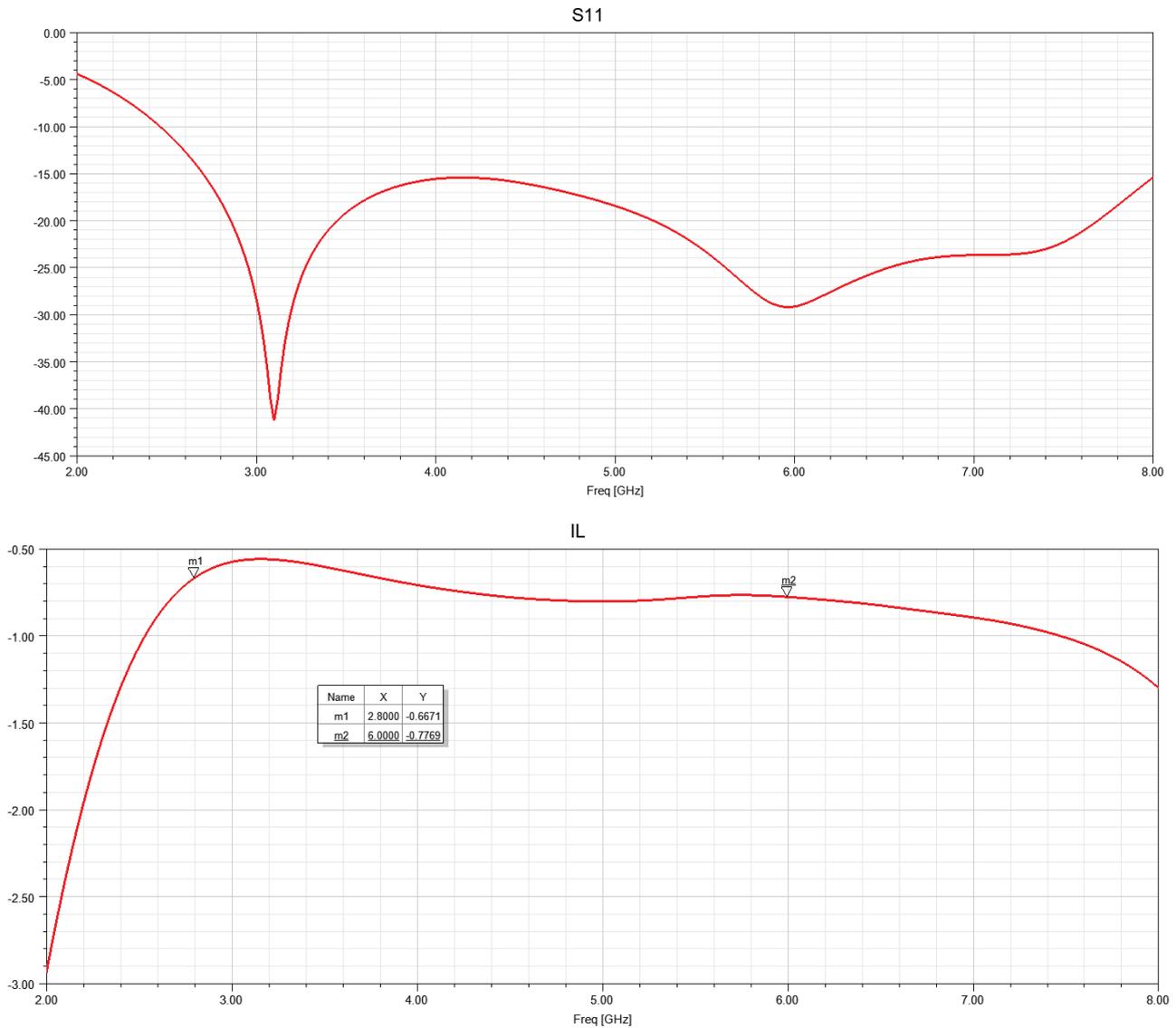


With DC Feed

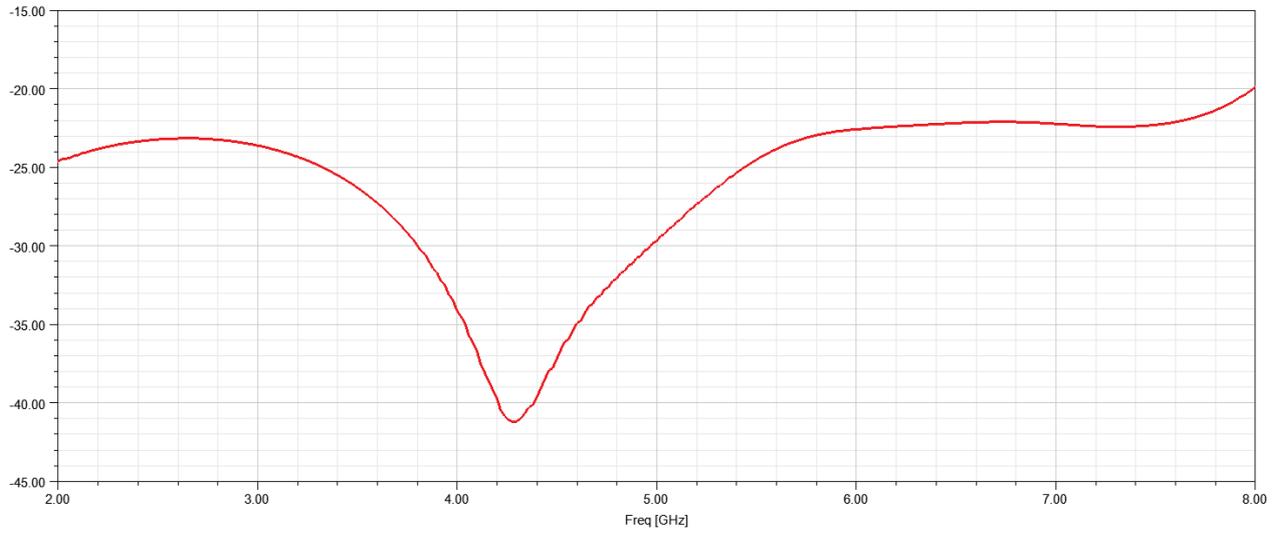


Line width to be design to match 50W characteristic impedance, depending on PCB material and thickness

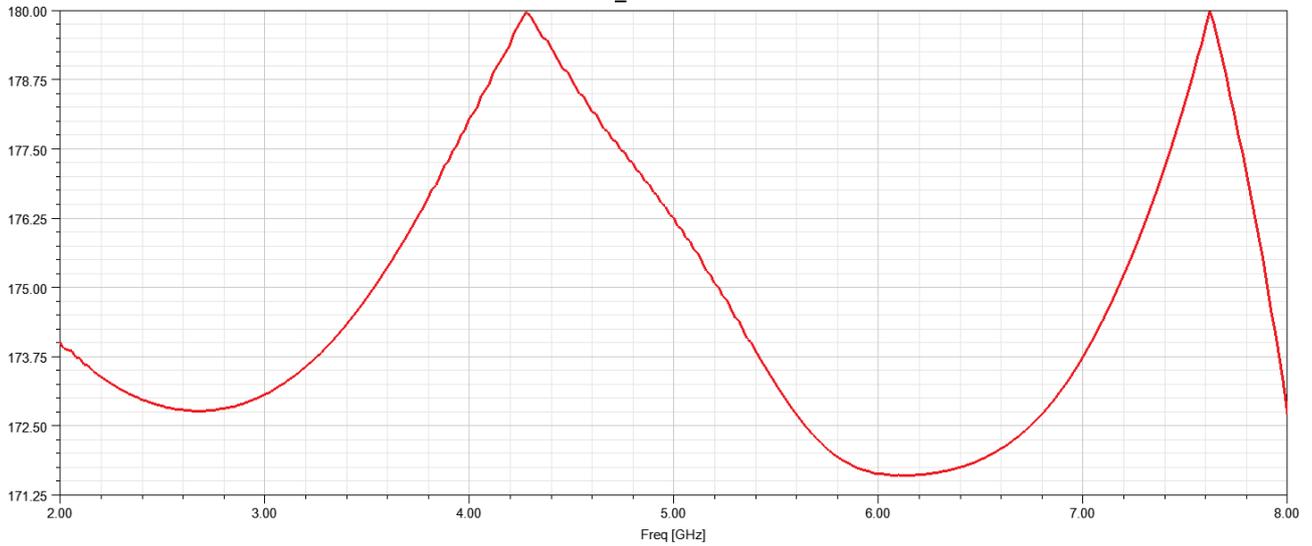
Typical Electrical Characteristics (T=25°C)



CMRR



Phase_Unbalance



Amplitude_Unbalance

